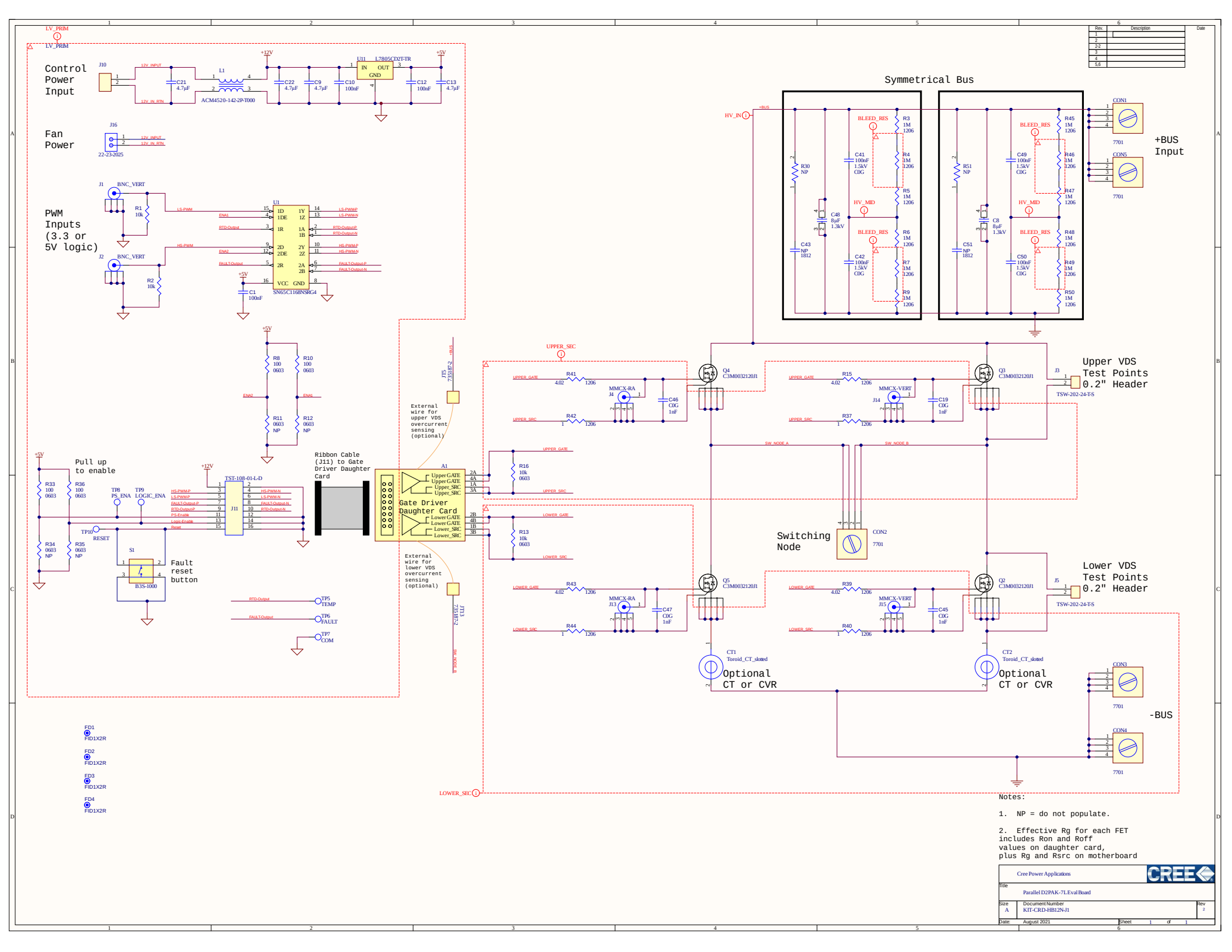
Control Power Input
Fan Power
PWM Inputs (3.3 or 5V logic)
Pull up to enable
Fault reset button
Ribbon Cable (J11) to Gate Driver Daughter Card
Gate Driver Daughter Card
External wire for upper VDS overcurrent sensing (optional)
External wire for lower VDS overcurrent sensing (optional)
Symmetrical Bus
+BUS Input
-BUS
Upper VDS Test Points 0.2" Header
Lower VDS Test Points 0.2" Header
Switching Node
Optional CT or CVR
Optional CT or CVR
Notes:
1. NP = do not populate.
2. Effective Rg for each FET includes Ron and Roff values on daughter card, plus Rg and Rsrc on motherboard
Cree Power Applications
Parallel D2PAK-7L Eval Board
KIT-CRD-HB12N-J1